

FIG. 1

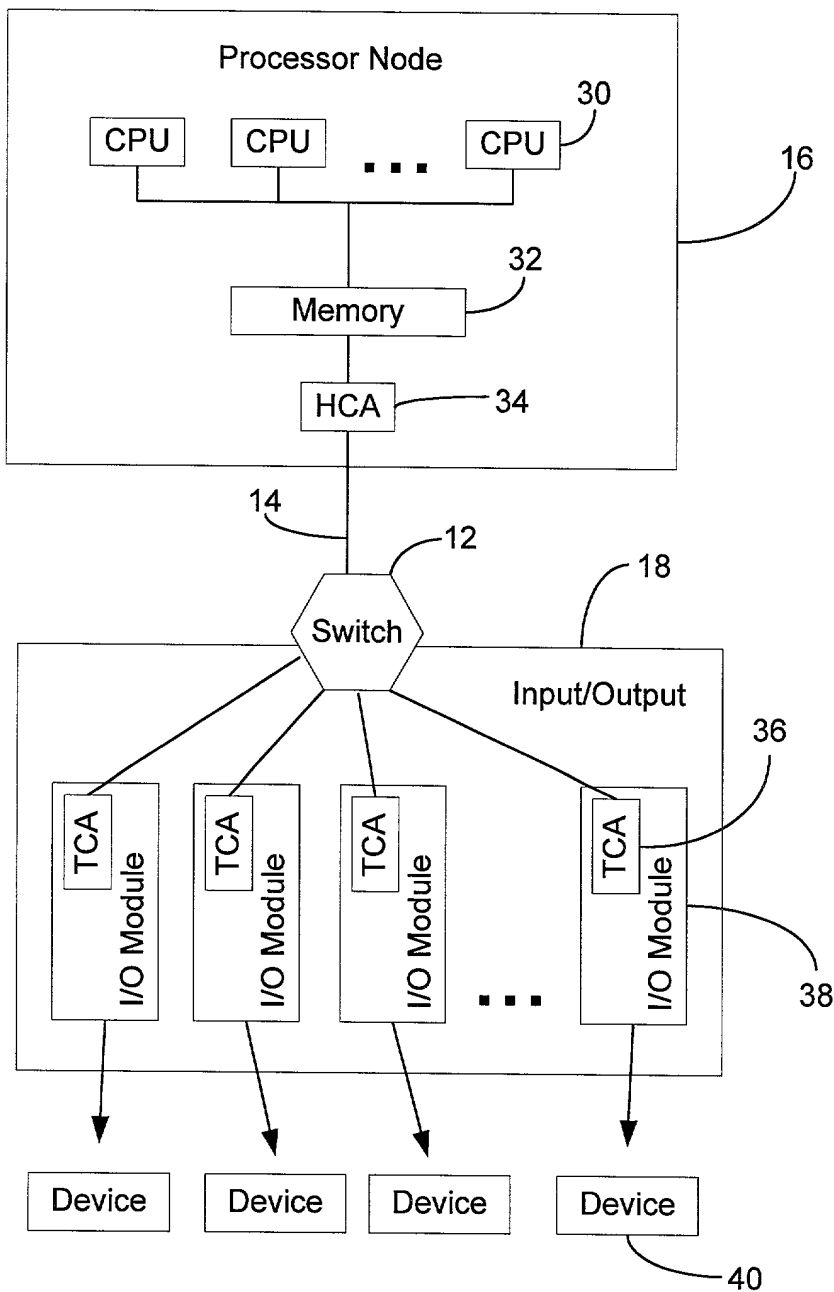


FIG. 2

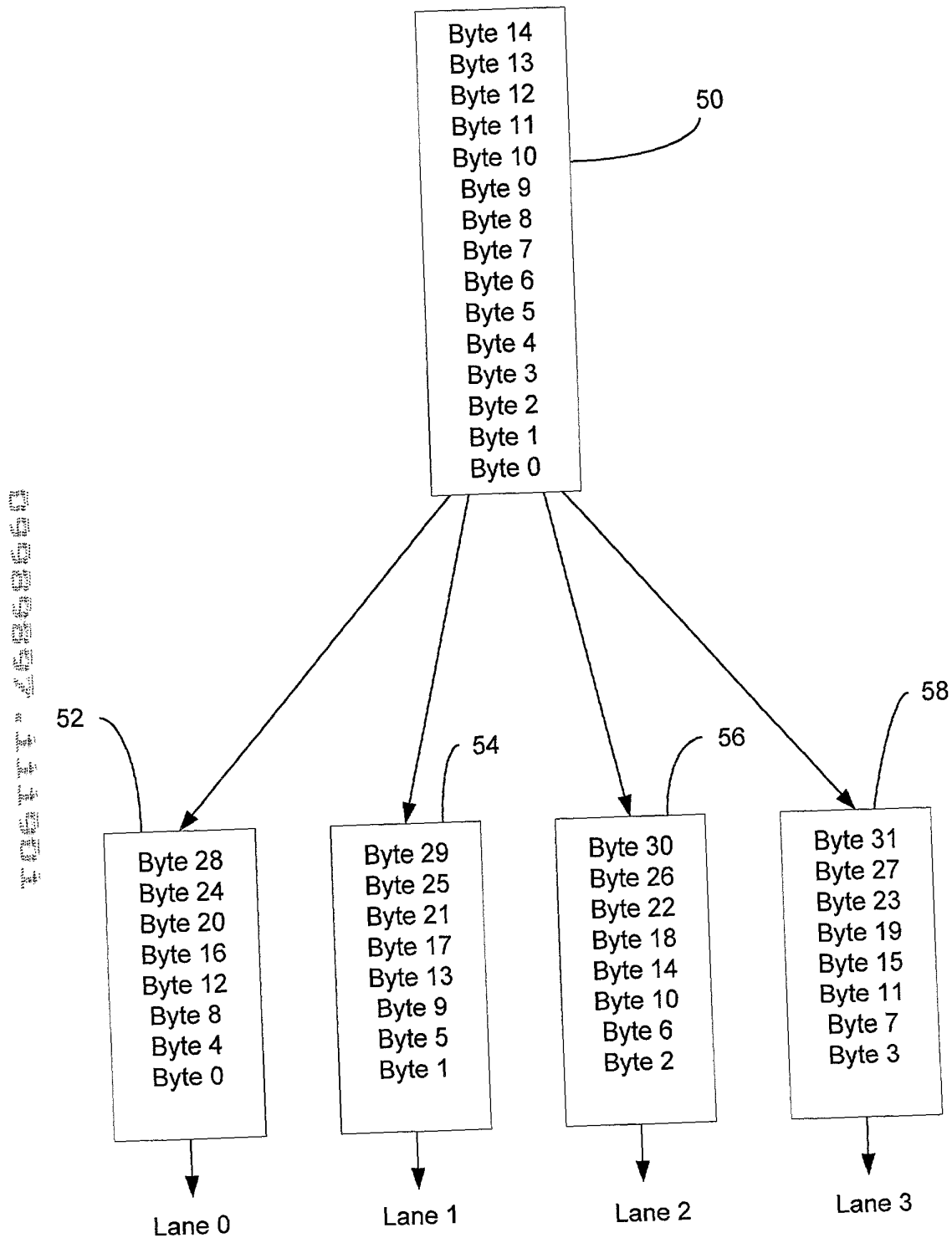


FIG. 3

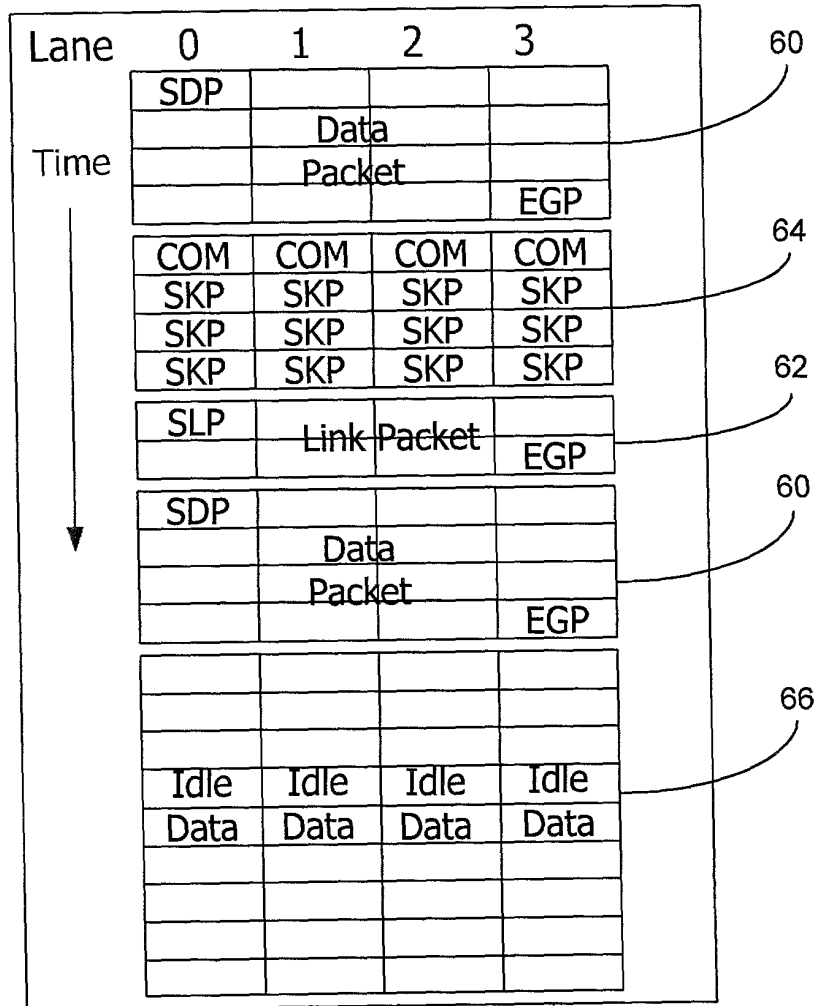


FIG. 4

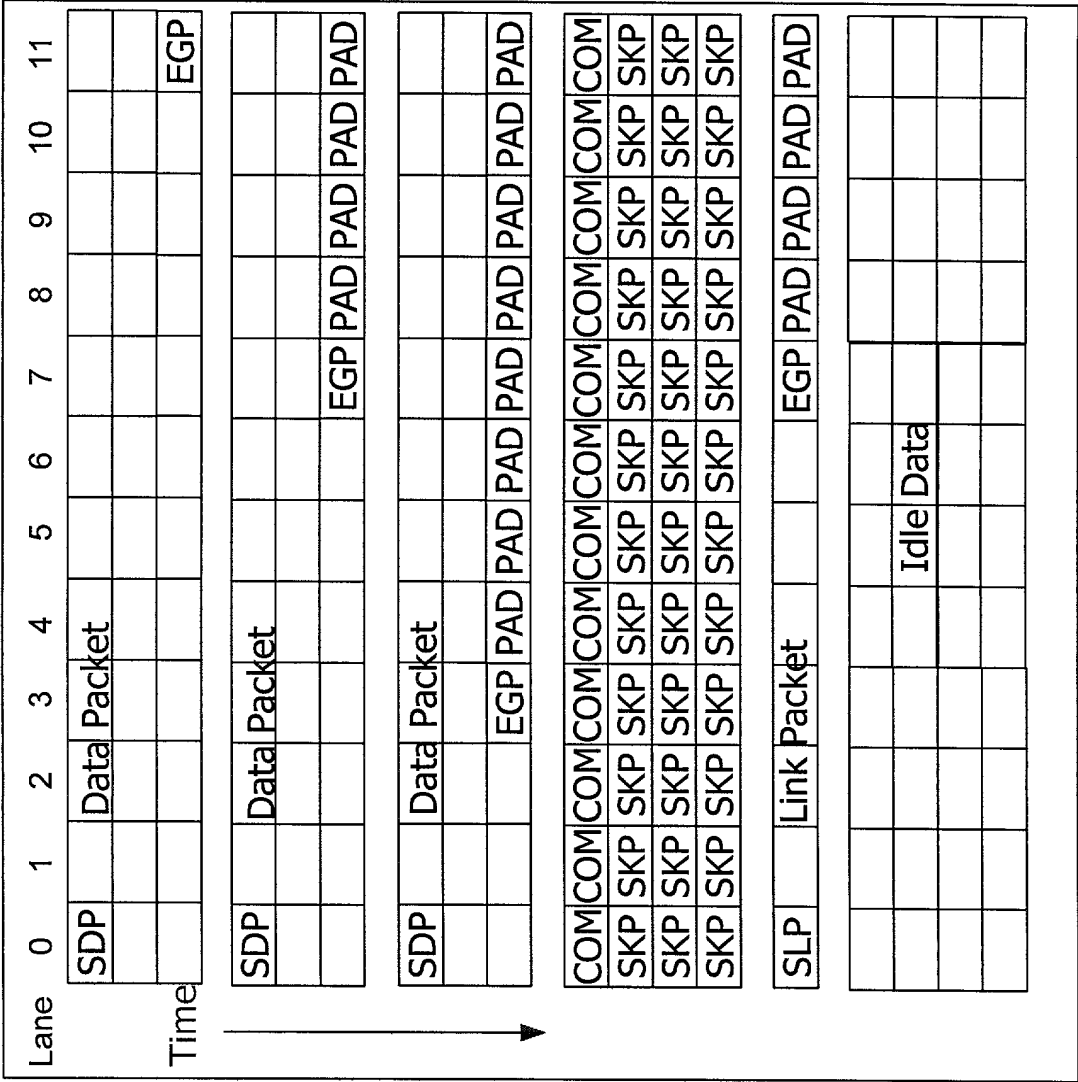


FIG. 5

SKIP ordered set

COM	COM	COM	COM
SKP	SKP	SKP	SKP
SKP	SKP	SKP	SKP
SKP	SKP	SKP	SKP

TS1 ordered set

COM	COM	COM	COM
00	01	02	04
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A
4A	4A	4A	4A

Skip Control Symbols

Lane Number Data

TS1 Data

TS2 ordered set

COM	COM	COM	COM
00	01	02	04
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45
45	45	45	45

FIG. 6

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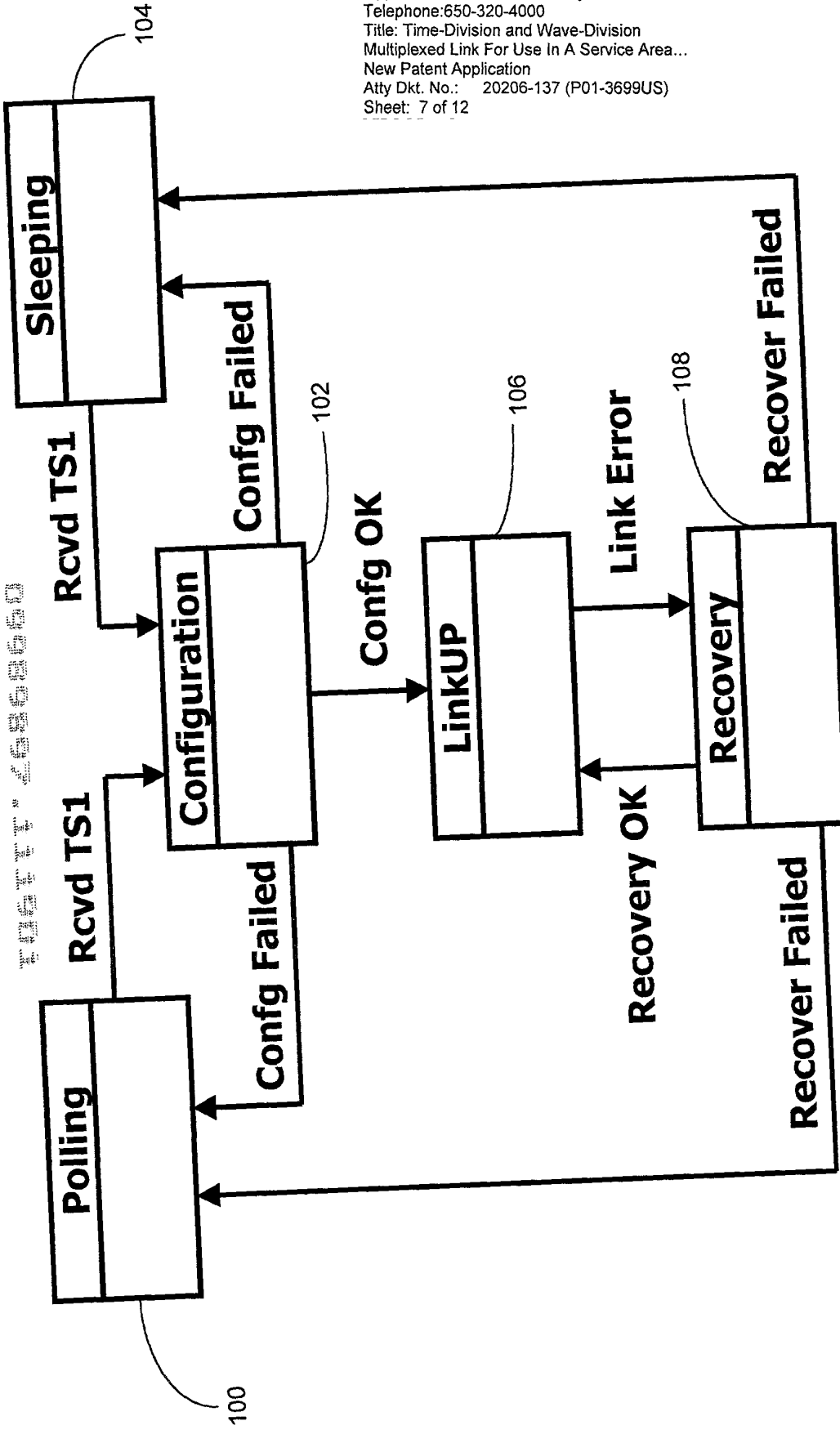


FIG. 7

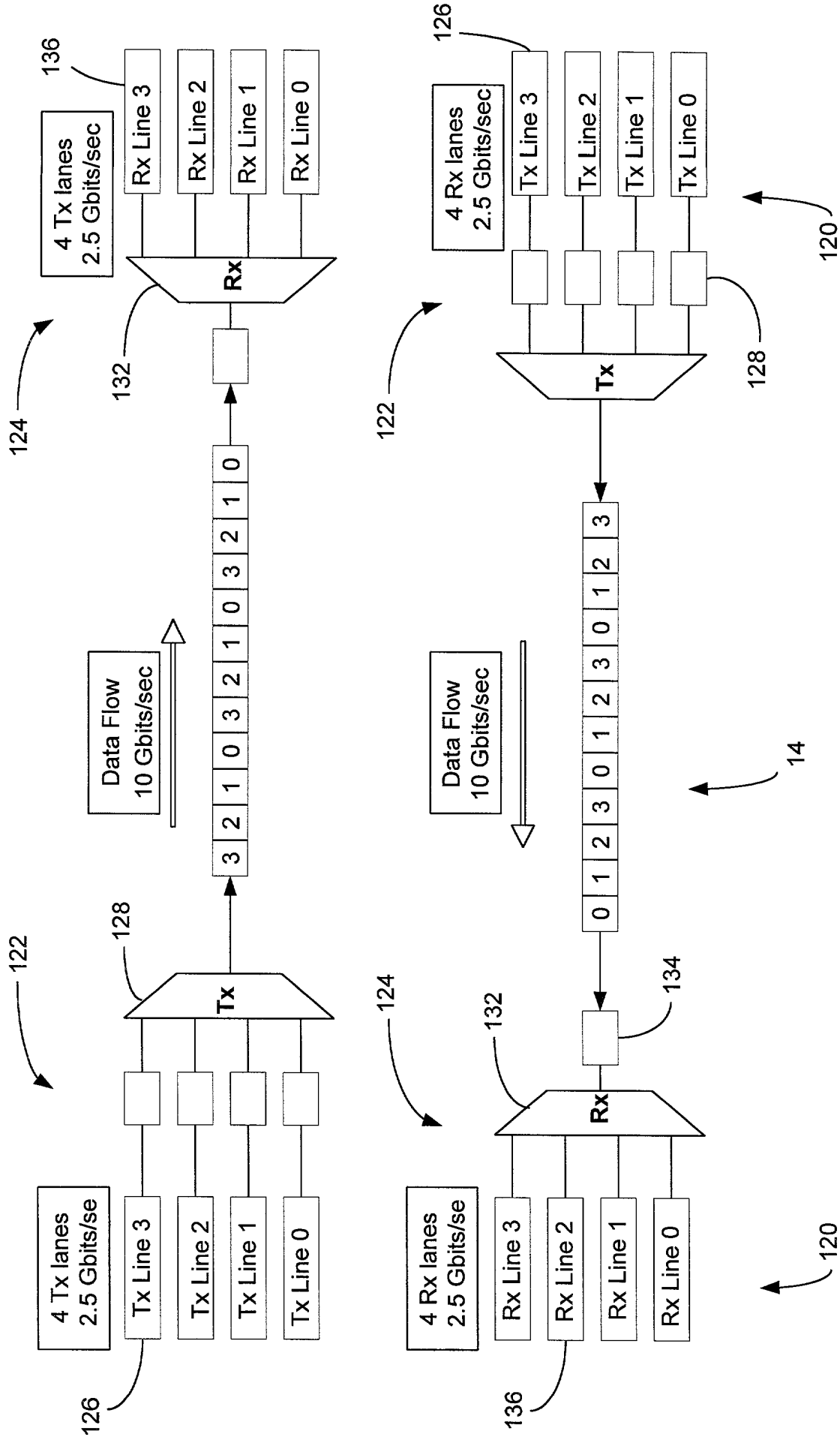


FIG. 8

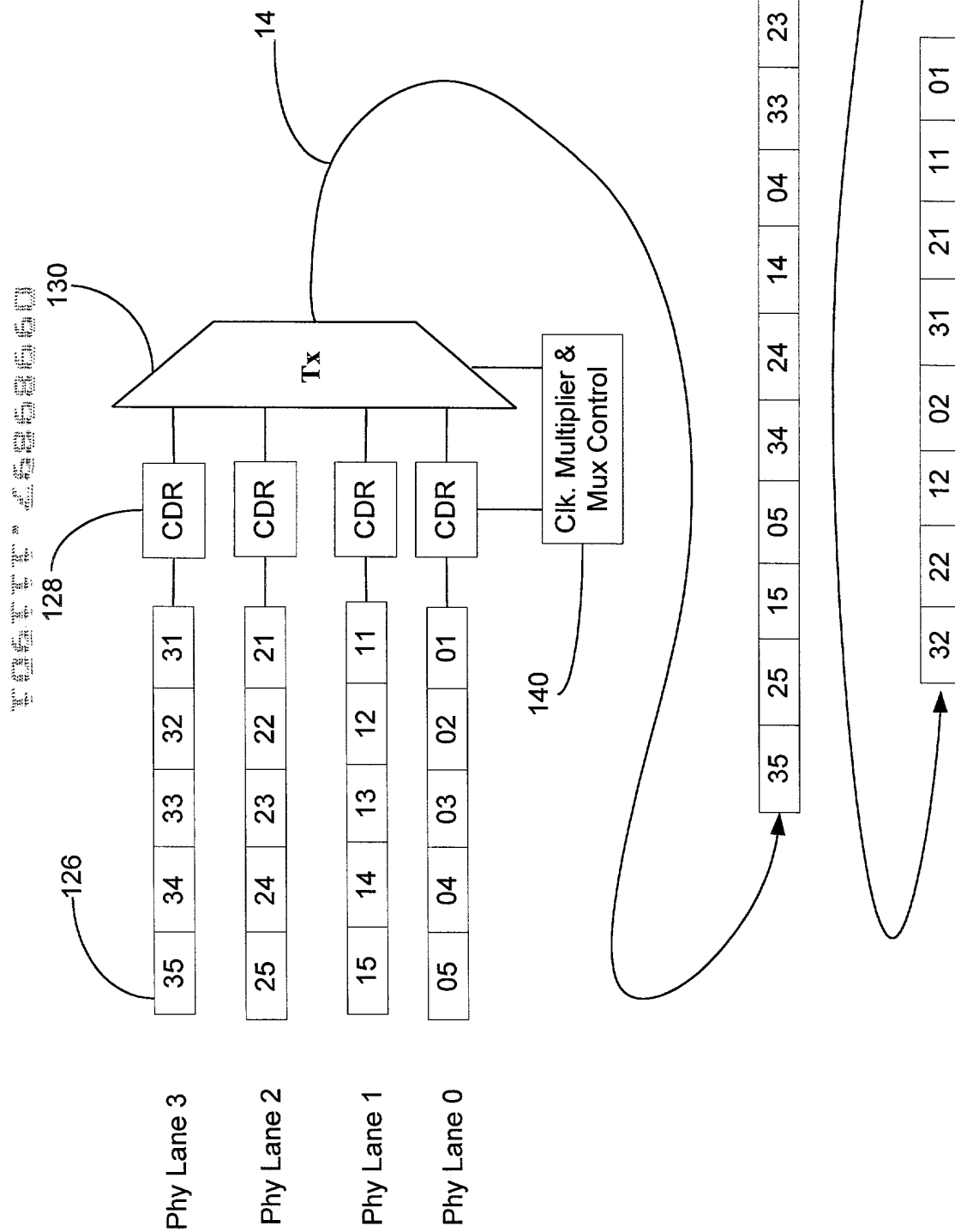


FIG. 9

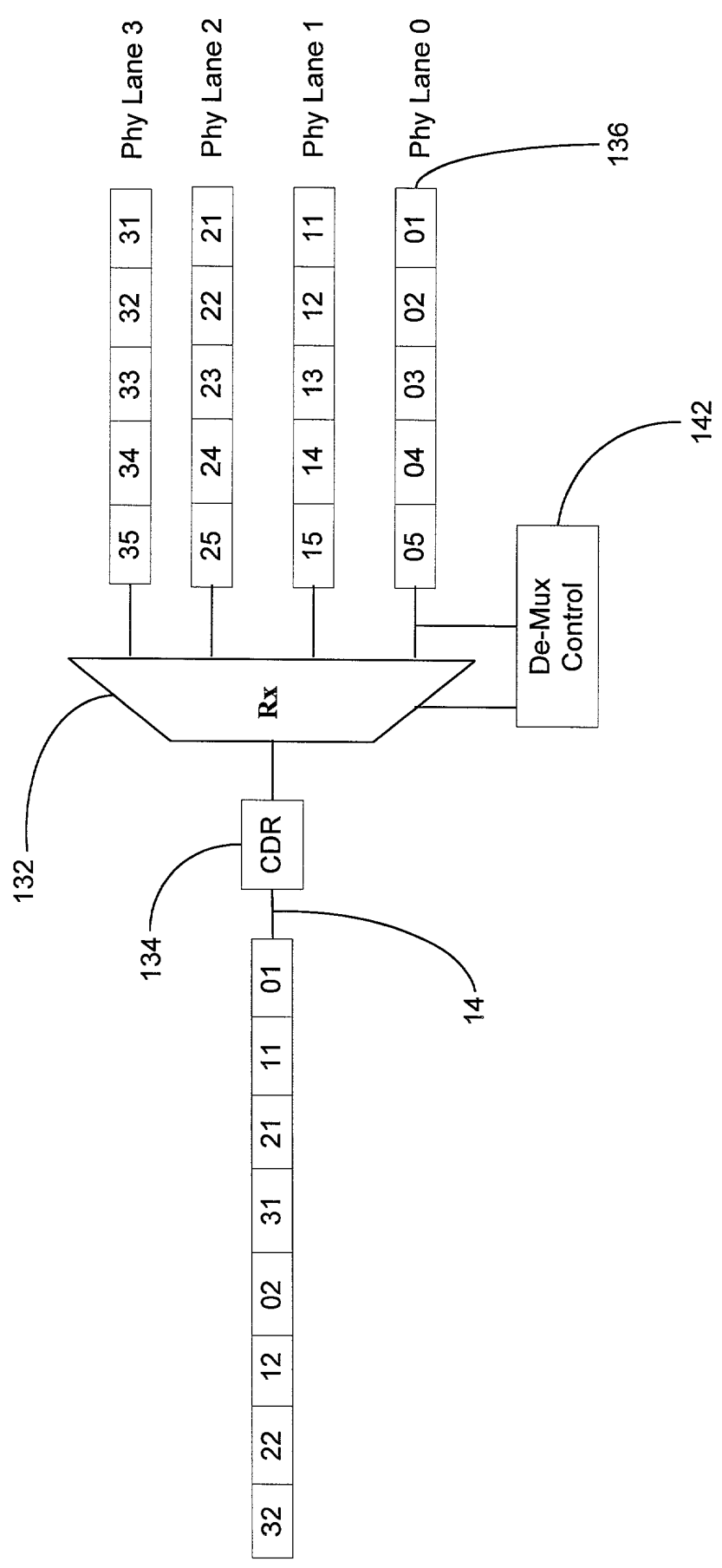


FIG. 10

132
 134
 136
 142
 14

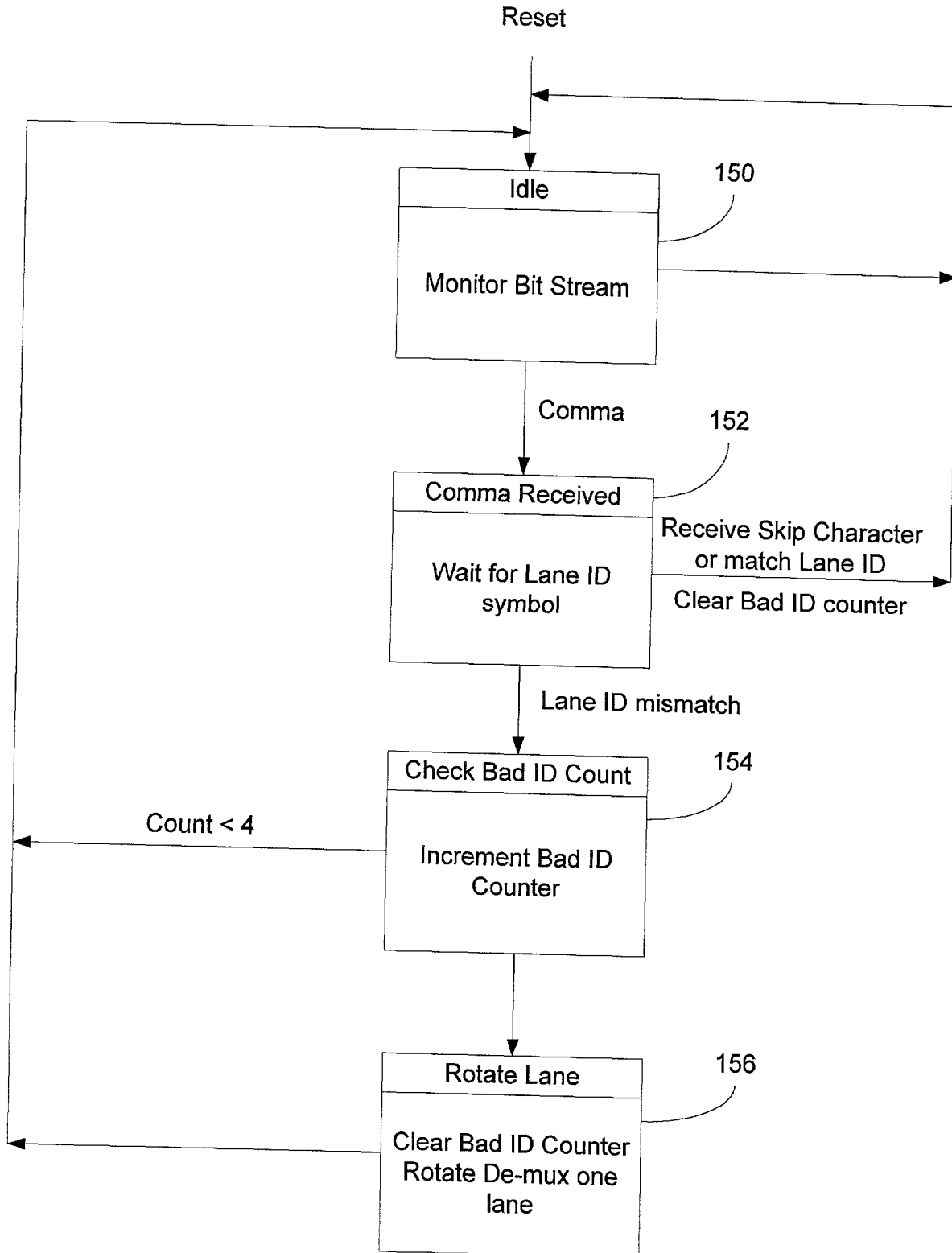


FIG. 11

160

12 Tx lanes
2.5 Gbits/sec

Tx Line 11
Tx Line 10
Tx Line 9
Tx Line 8

Tx

Data Flow
30 Gbits/sec

166

Tx Line 7
Tx Line 6
Tx Line 5
Tx Line 4

Tx

163

Tx Line 3
Tx Line 2
Tx Line 1
Tx Line 0

Tx

130

128

126

11	10	9	8	11	10	9	8	11	10	9	8
7	6	5	4	7	6	5	4	7	6	5	4
3	2	1	0	3	2	1	0	3	2	1	0

161

168

165

Rx Line 7
Rx Line 6
Rx Line 5
Rx Line 4

Rx

Rx Line 11
Rx Line 10
Rx Line 9
Rx Line 8

Rx

Rx Line 3
Rx Line 2
Rx Line 1
Rx Line 0

Rx

136

132

134

170

162

12 Rx lanes
2.5 Gbits/sec

FIG. 12